



2815  
Dkt. 2271/60617

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**RECEIVED**

Application of: Yoshinori UEDA

JUL 26 2004

Serial No.: 09/431,593

Group Art Unit: 2815

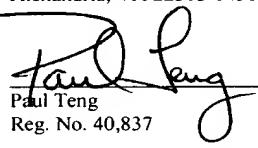
TECH CENTER 2800

Date Filed: November 1, 1999

Examiner: Matthew E. Warren

For: SEMICONDUCTOR DEVICE HAVING AN INTEGRAL RESISTANCE ELEMENT

I hereby certify that this correspondence is being deposited this date with the U.S. Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
Paul Teng  
Reg. No. 40,837

July 20, 2004  
Date

1185 Avenue of the Americas  
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(212) 278-0400

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**AMENDMENT**

This Amendment is submitted in response to the Office Action dated April 20, 2004 in connection with the above-identified application.

Amendments to the claims are reflected in the **Listing of Claims** section which begins on page 2 of this paper.

**Remarks** begin on page 5 of this paper.